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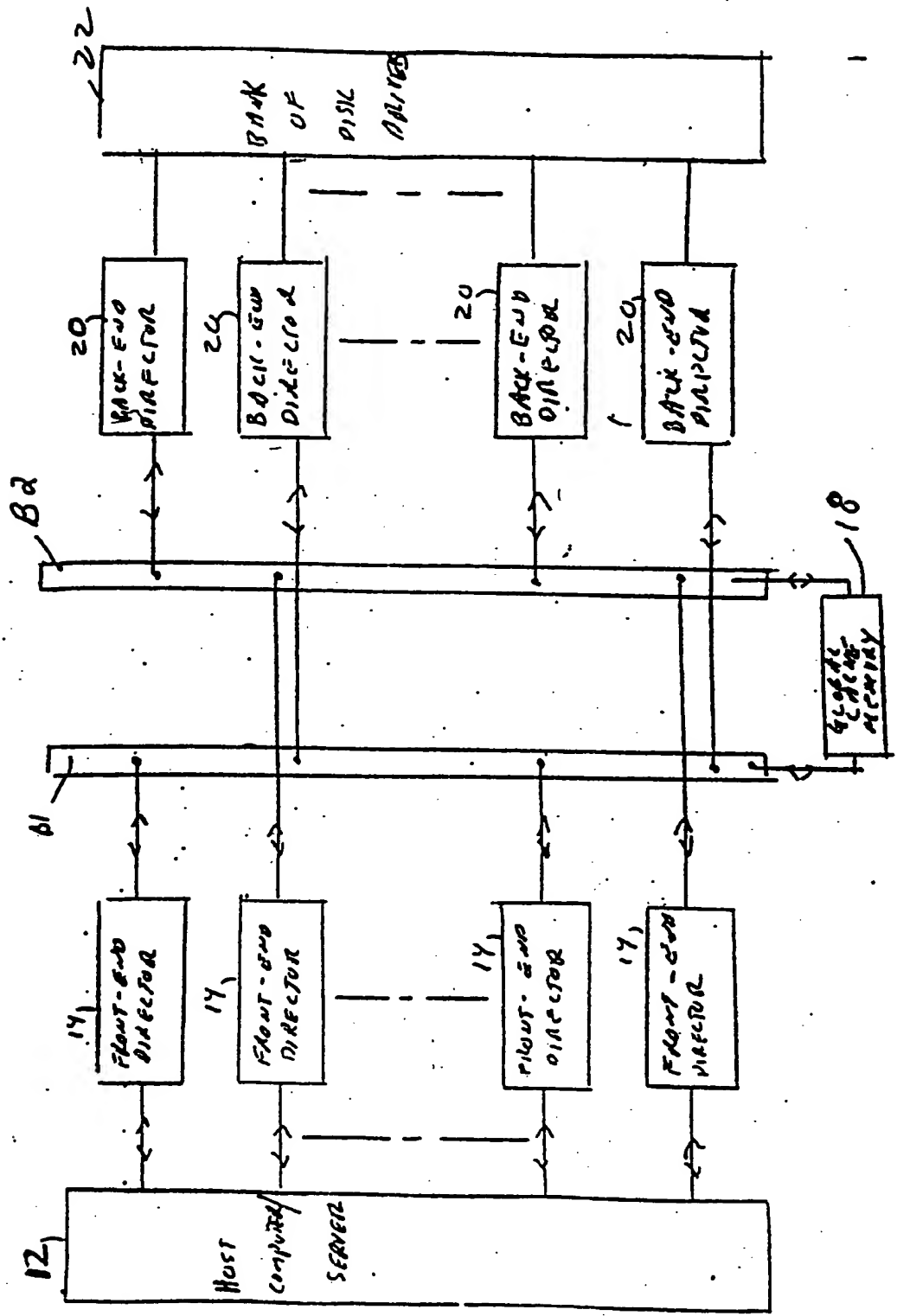
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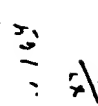
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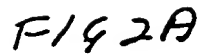
FIG. 1

FIG. 1



John K. Walton
EMC2-090PUS





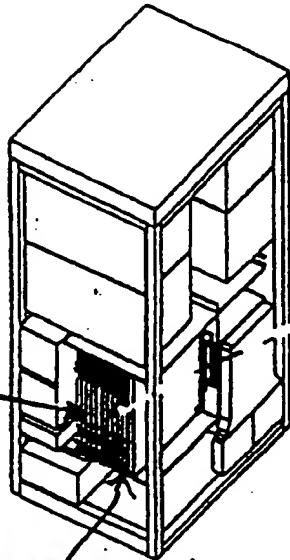


FIG. 3

DIRECTOR
BOARD

CACHE
MEMORY 220

BACK PLANE 302

TO/ FROM
DISK DRIVES 40
TO/ FROM
HOST COMPUTER/
SERVER/
HOST PROCESSOR
120

MESSAGE NETWORK
BOARD 304

MESSAGE
NETWORK
BOARD 3042

DIRECTOR
BOARD

DIRECTOR BOARD

DIRECTOR BOARD

DIRECTOR
BOARD

DIRECTOR
BOARD

BACK PLANE 302

CACHE MEMORY BOARD

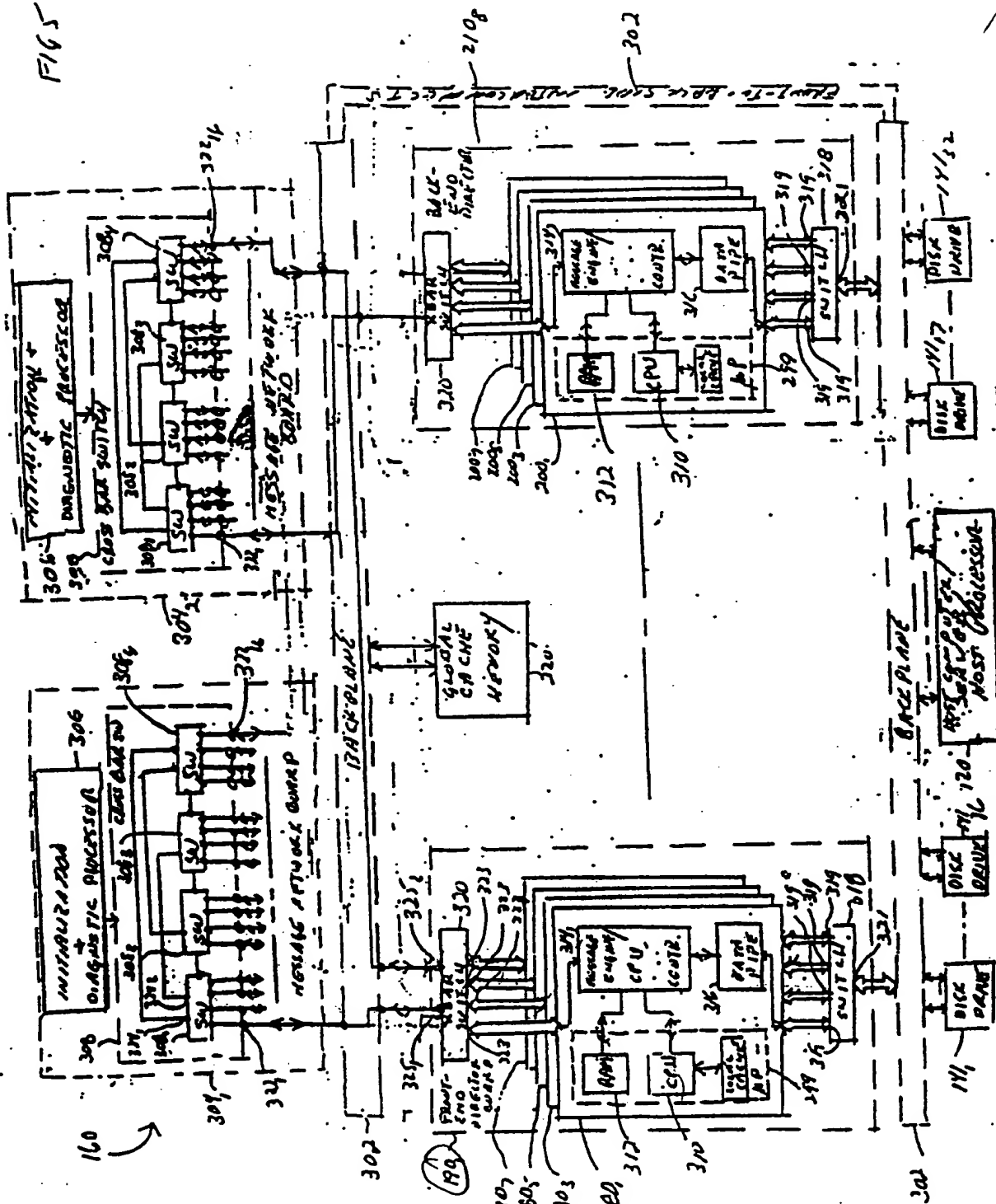
CACHE
MEMORY
BOARD

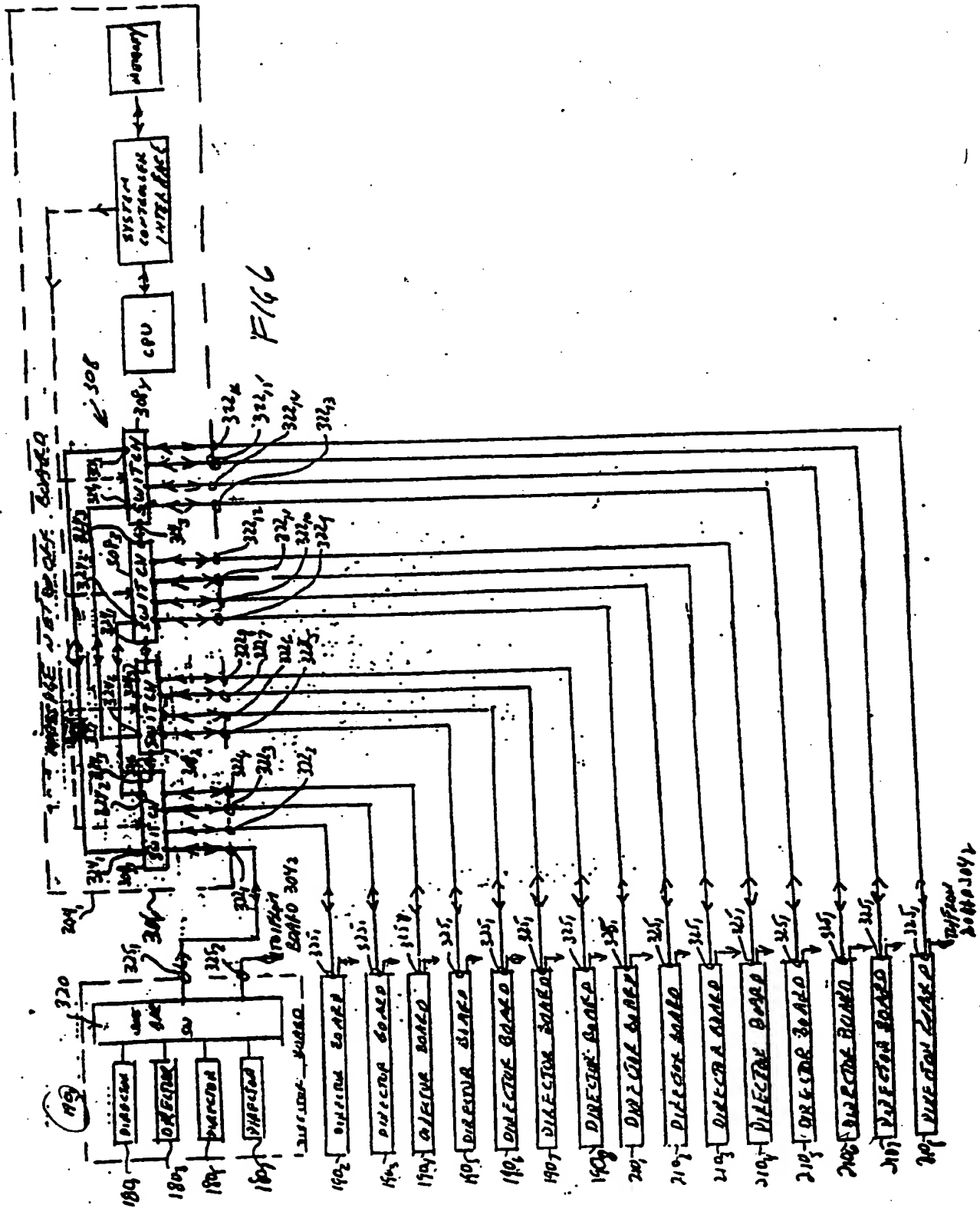
DIRECTOR
BOARD

09859559.051701
TOTAL 6595960

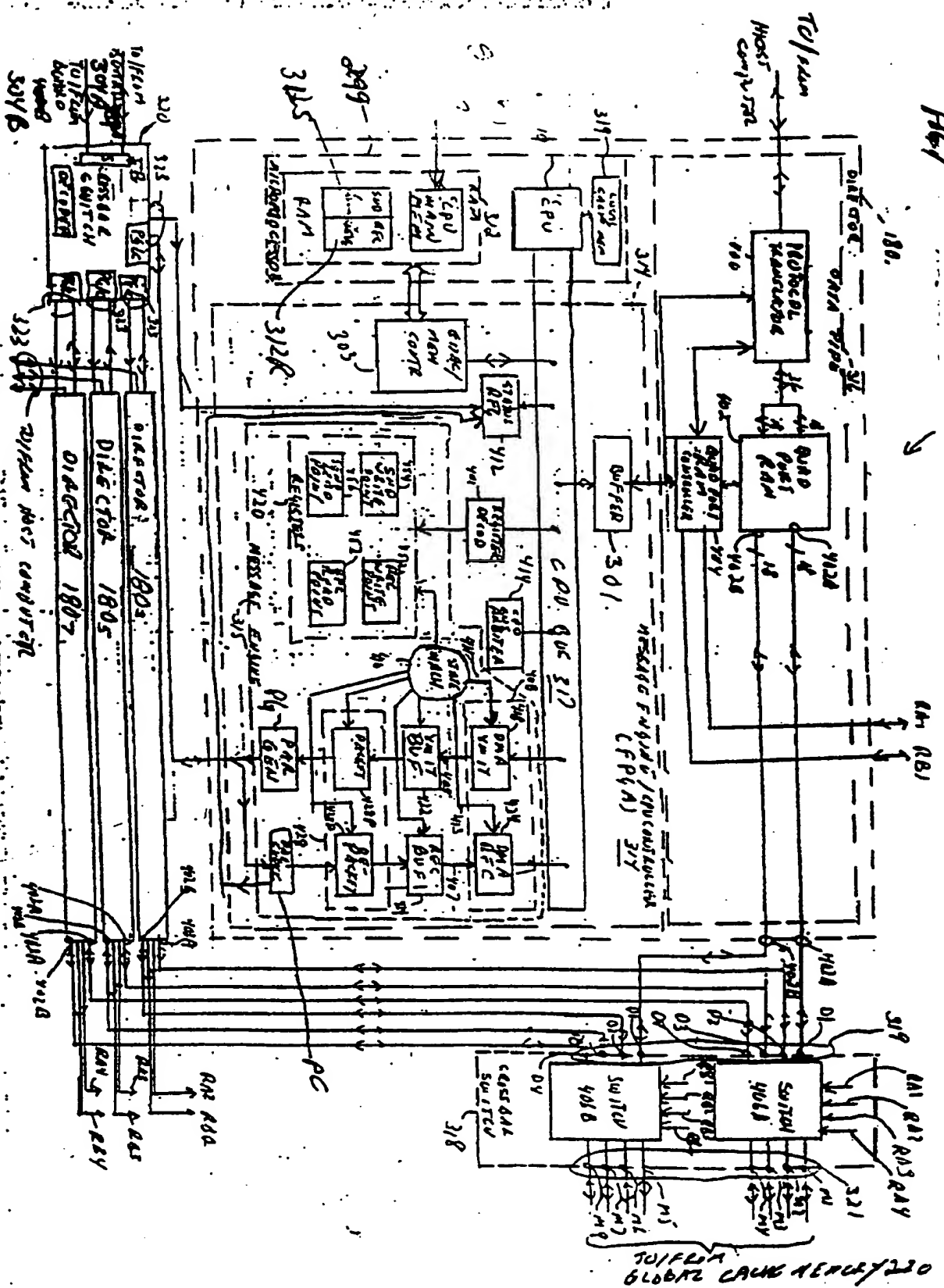
05059659 051701

FIG 5





H67



09859659 .051701

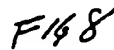
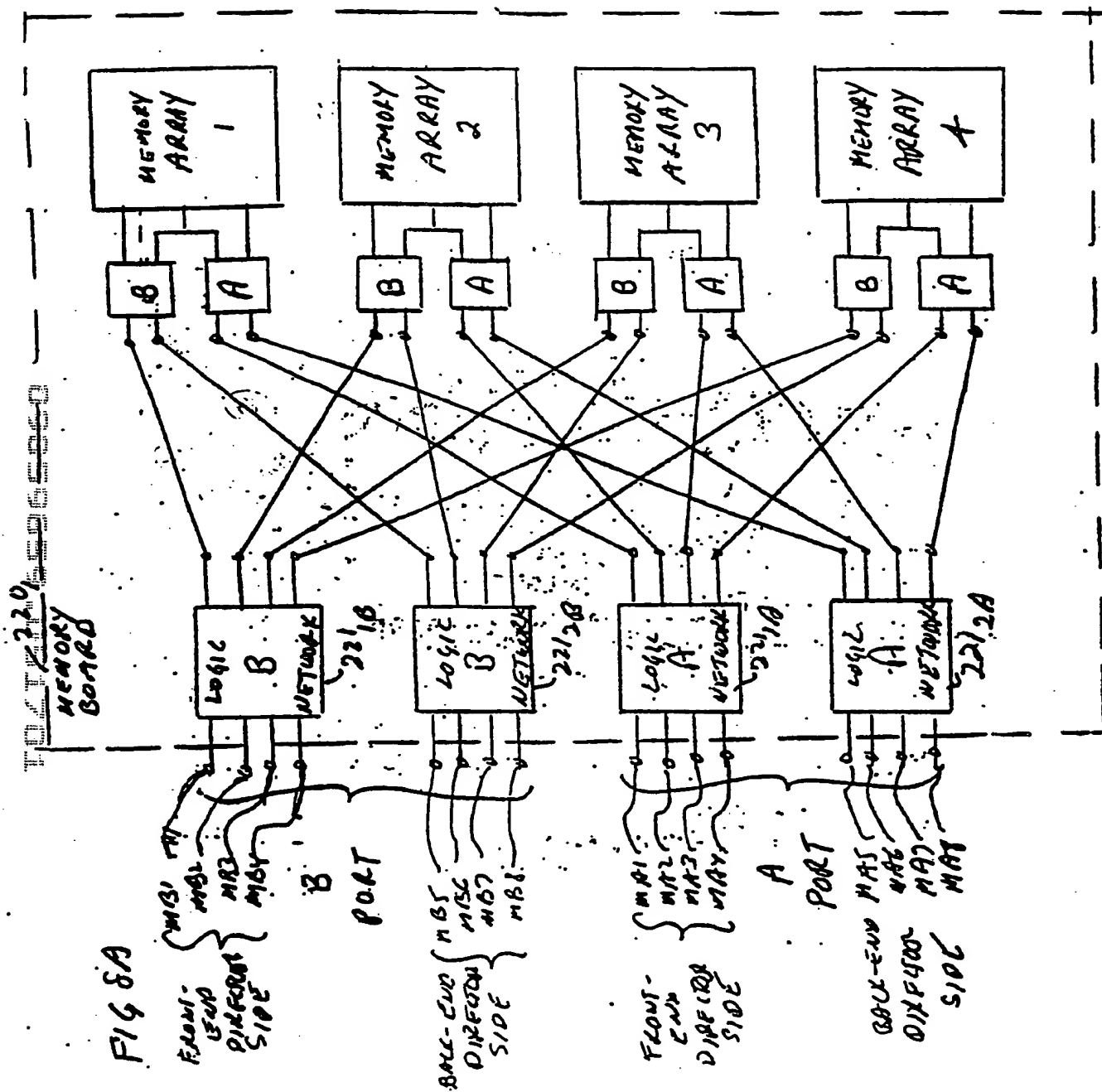
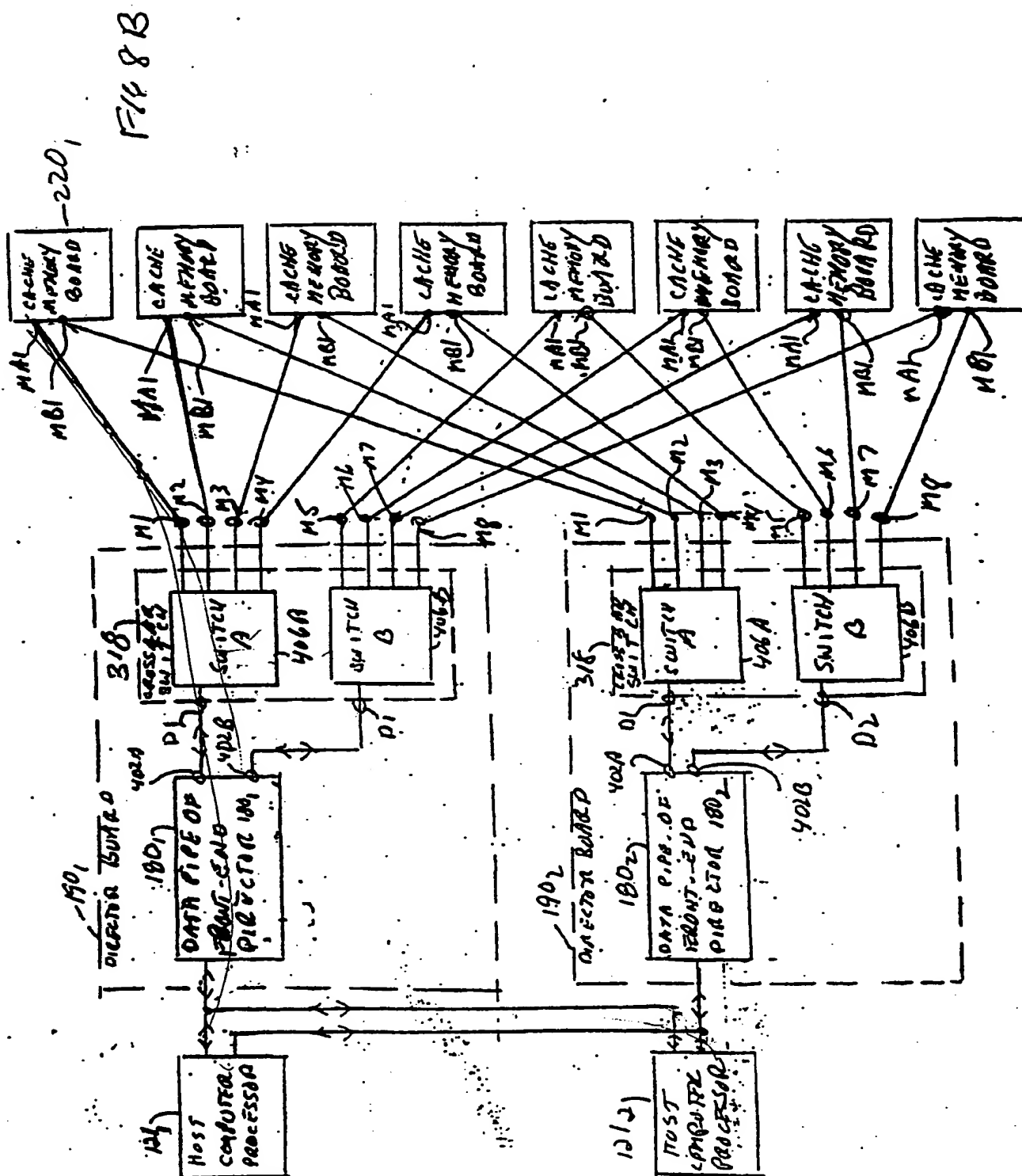


FIG 8A





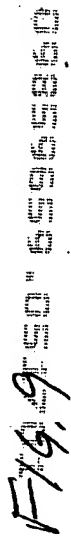
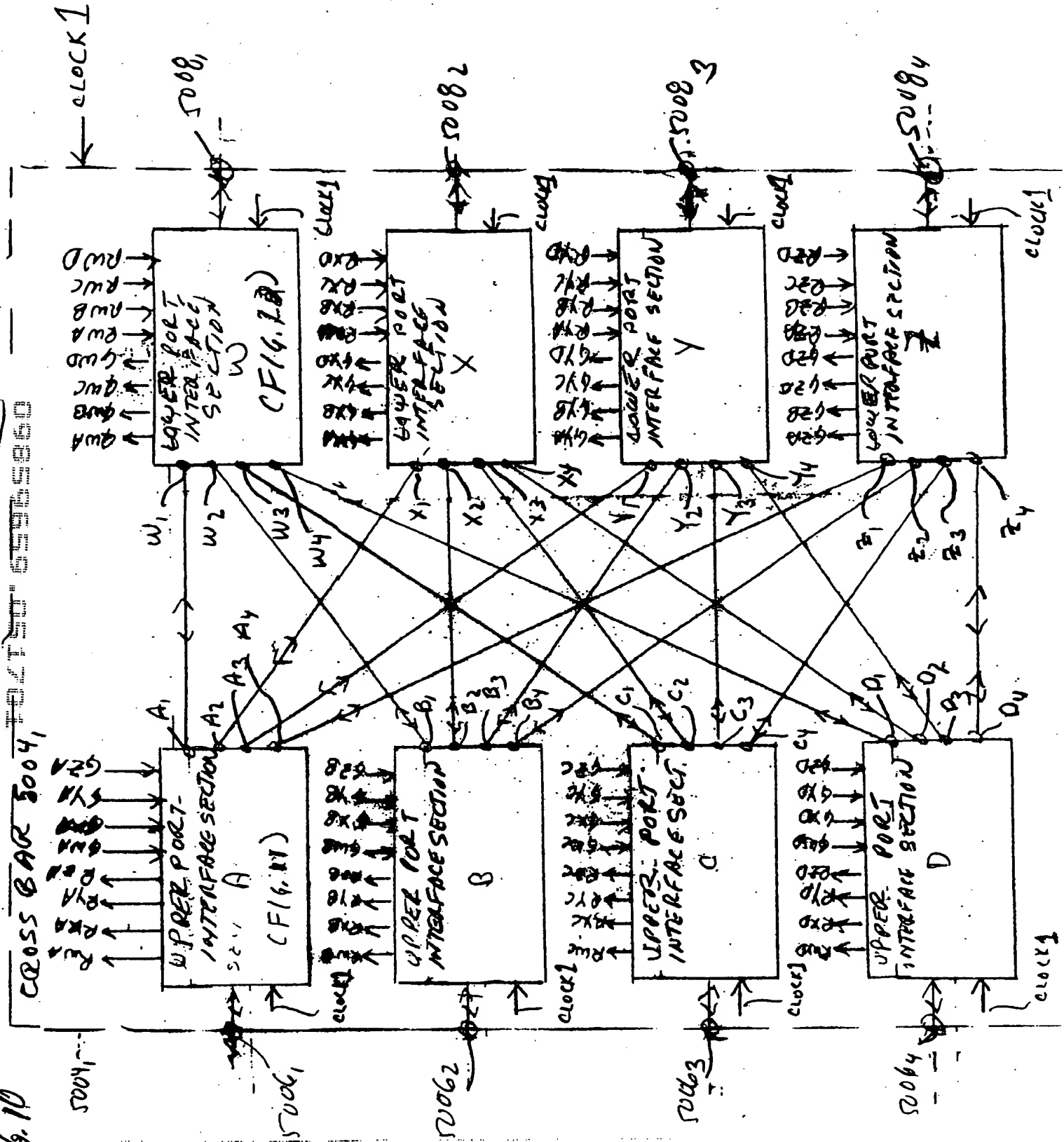


Fig. 10



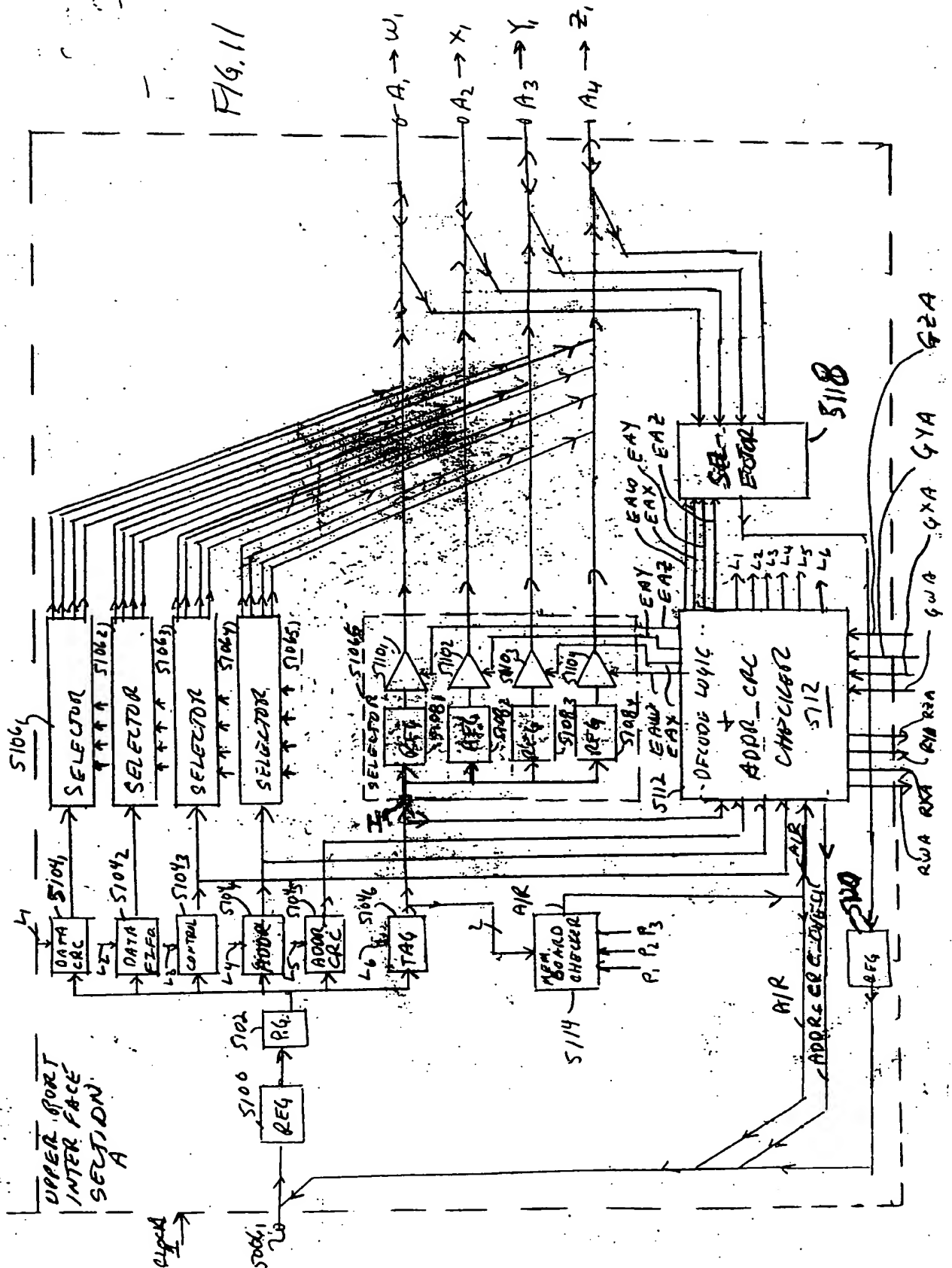
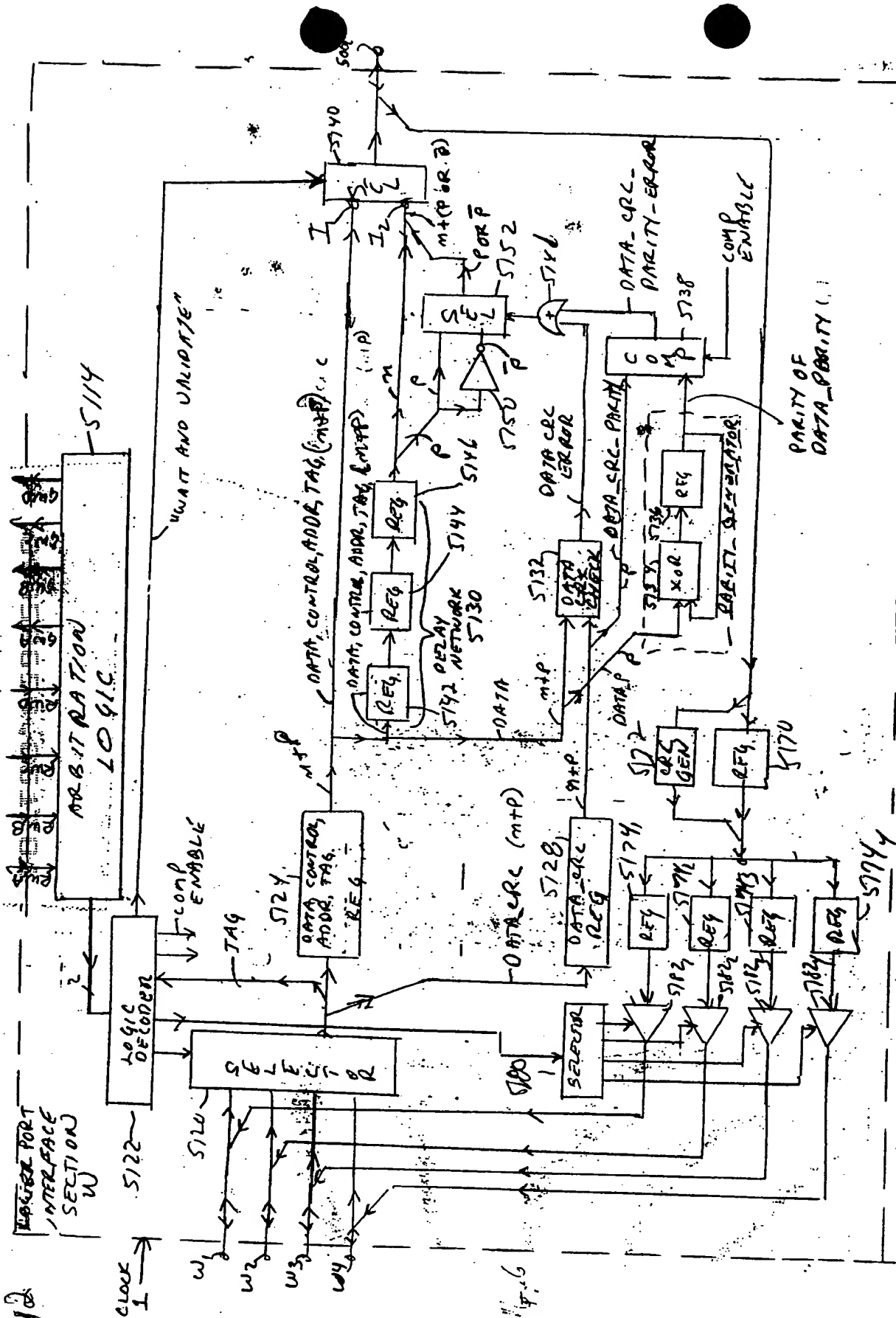
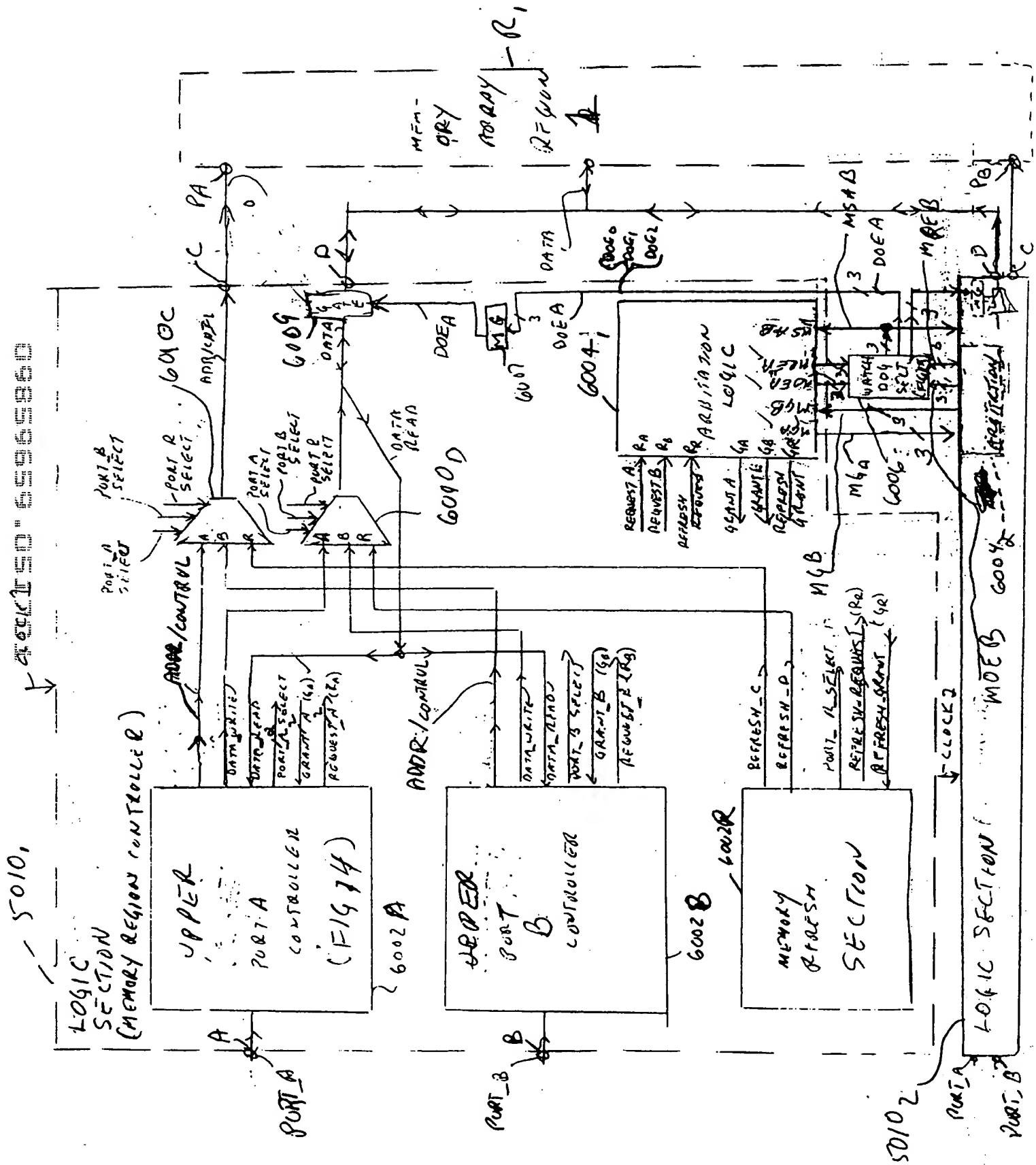
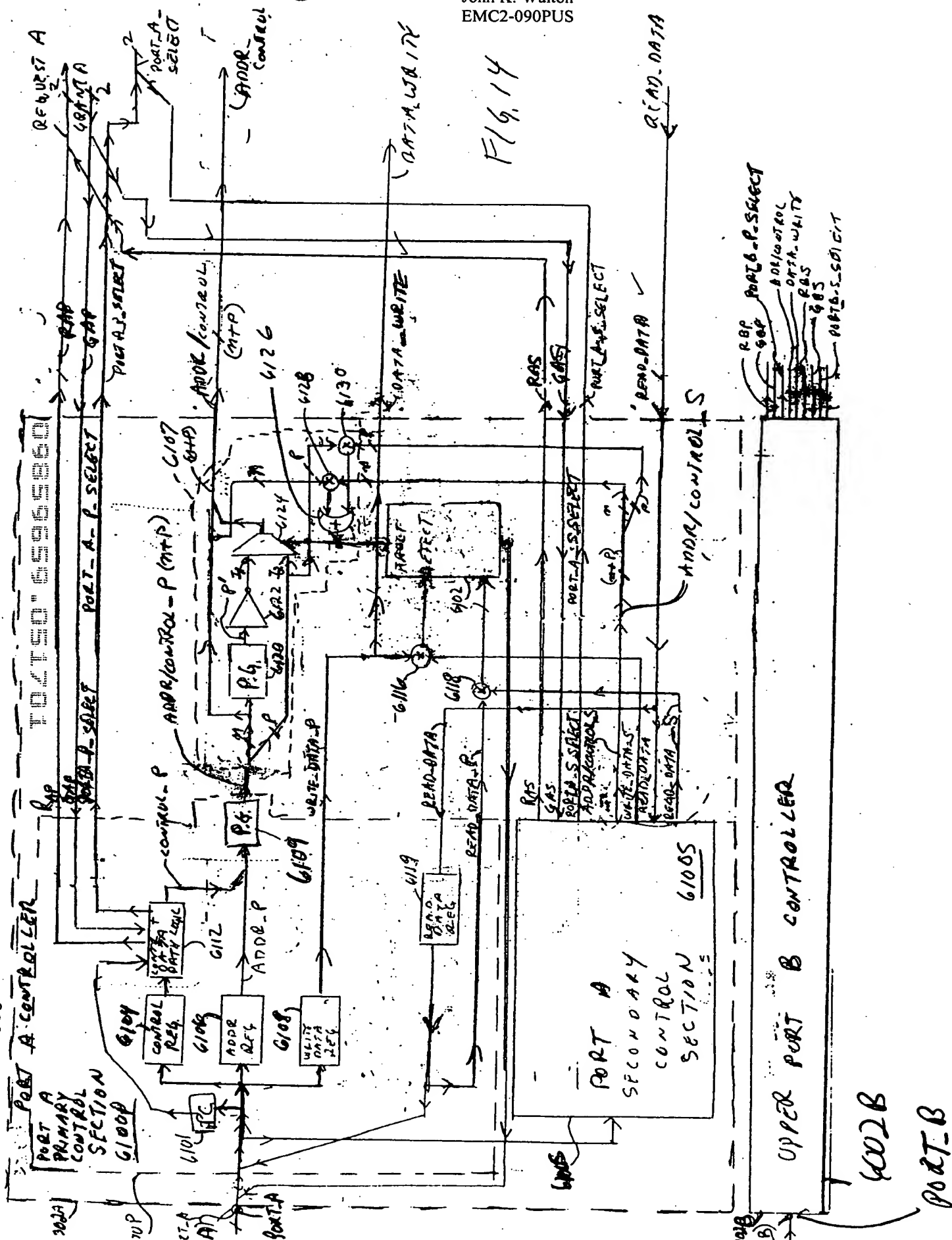


FIG. 10



1-250-12





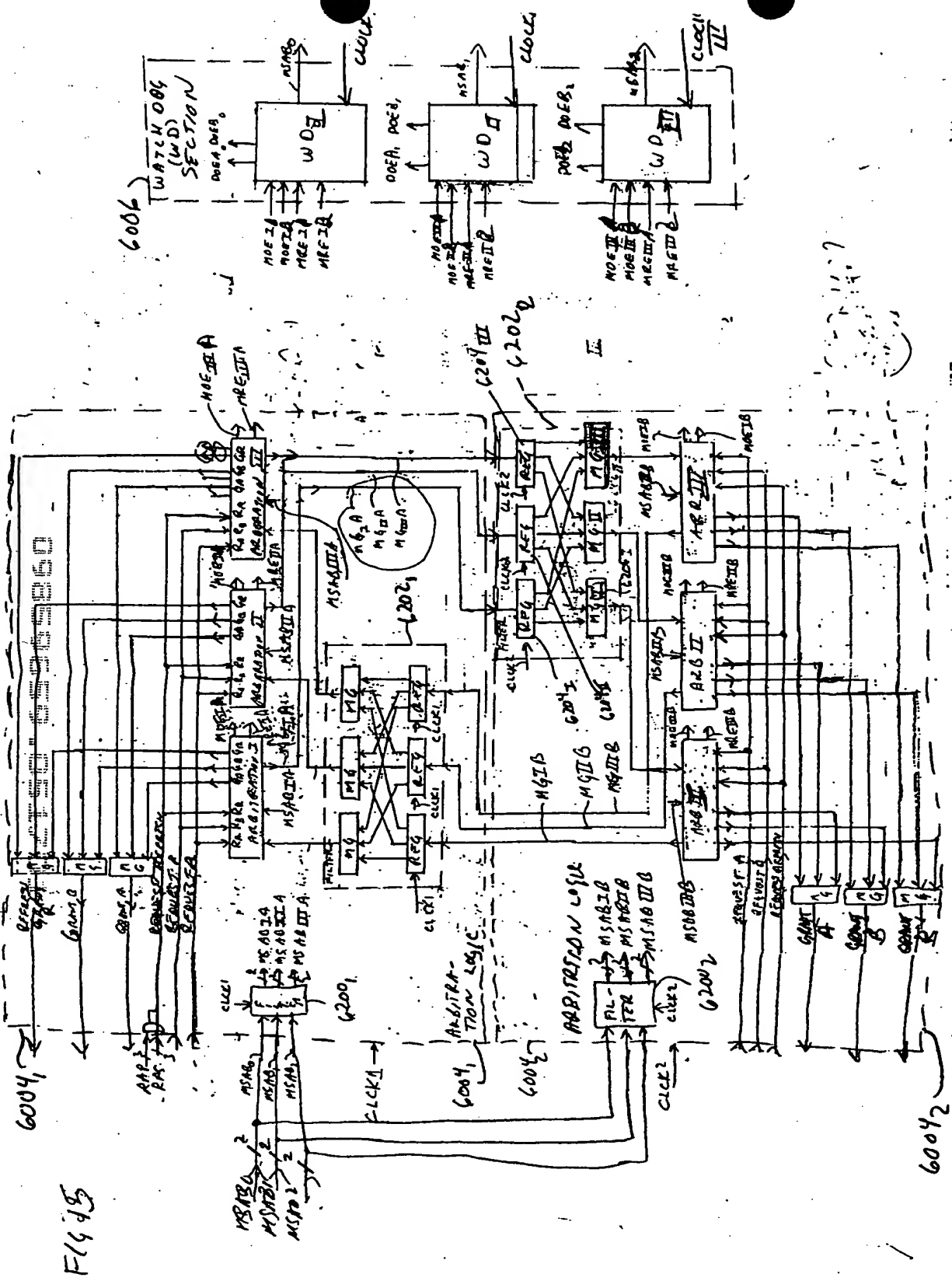


Fig 18

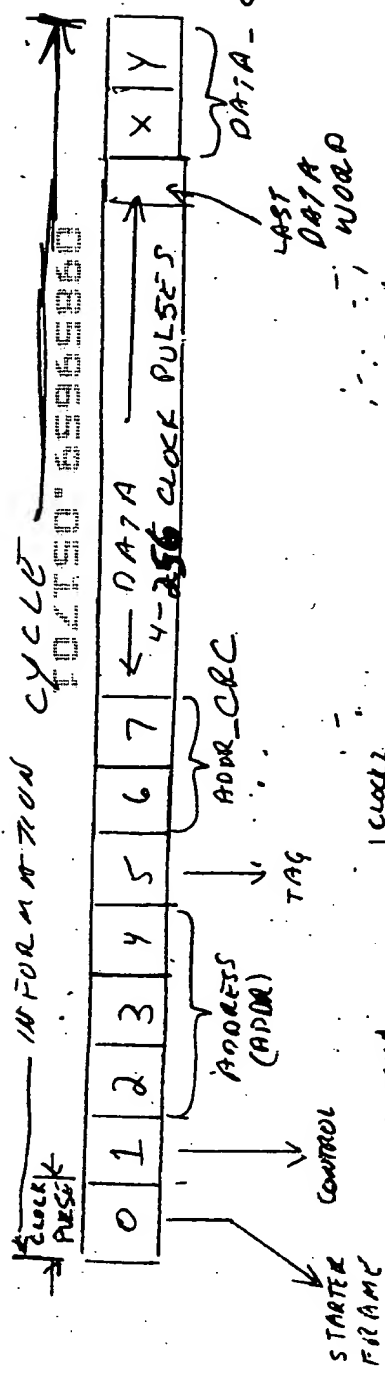


FIG. 16

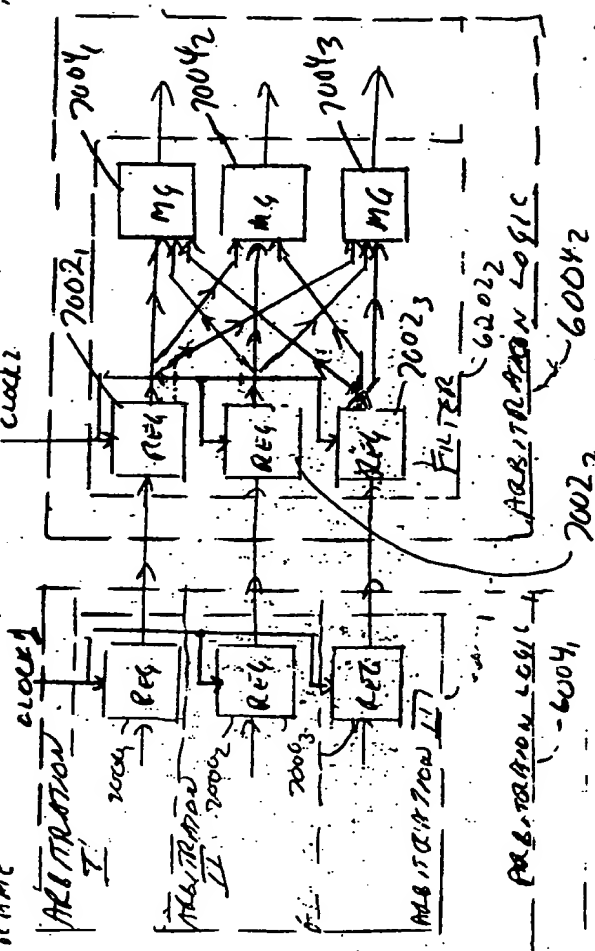


FIG. 17

MSG INPUTS	MSG OUTPUT	MSG OUTPUT	MSG OUTPUT
0 0 0	0	0	0
0 0 1	0	0	0
0 1 0	0	0	0
0 1 1	1	1	1
1 0 0	0	0	0
1 0 1	1	1	1
1 1 0	1	1	1
1 1 1	1	1	1

FIG. 18

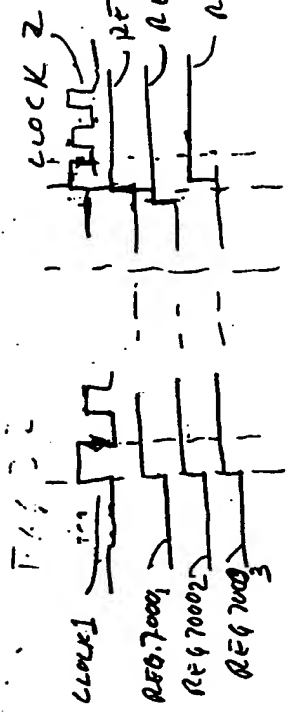


FIG. 19

[illegible]